



SYN1024SMPLL1

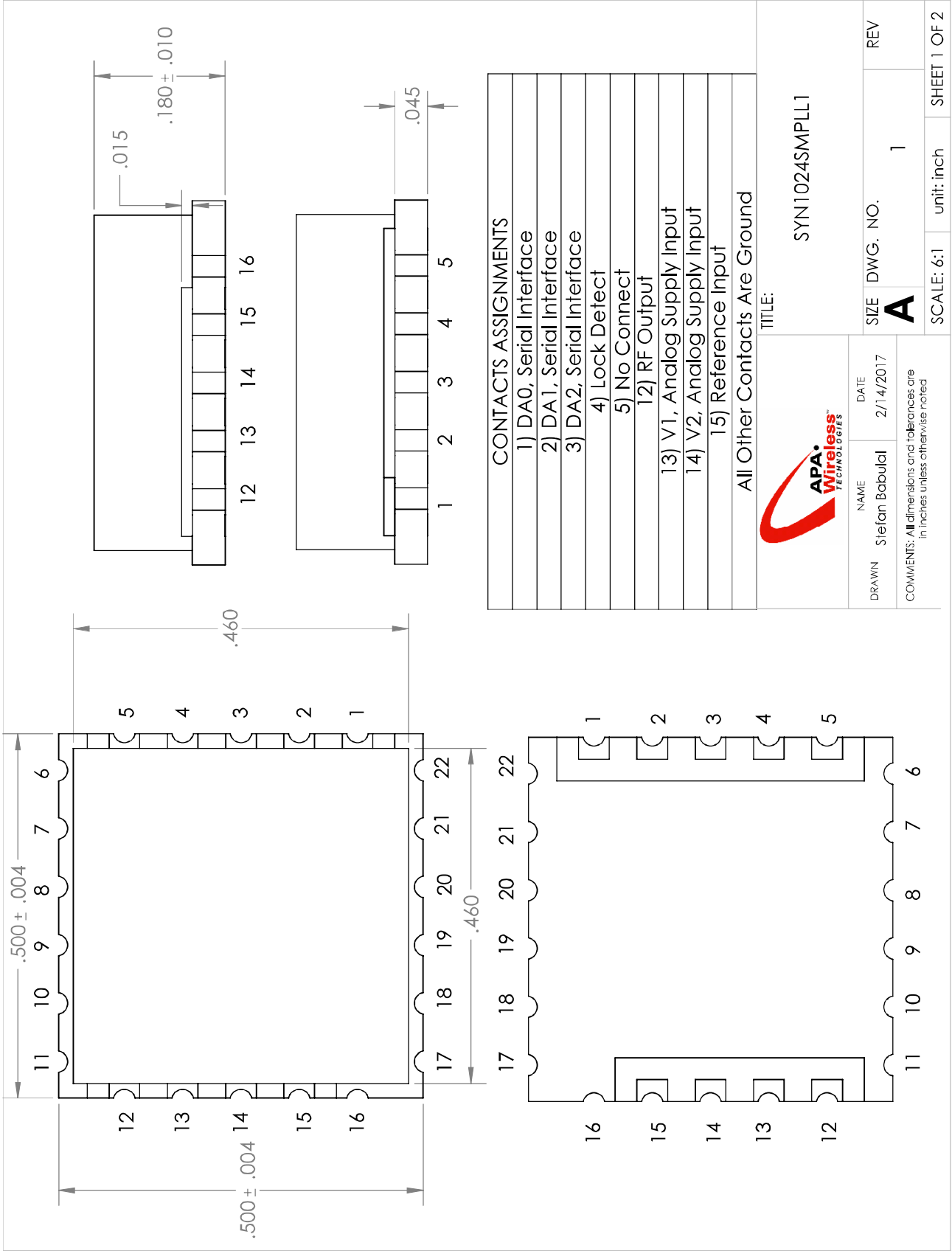
rev_0

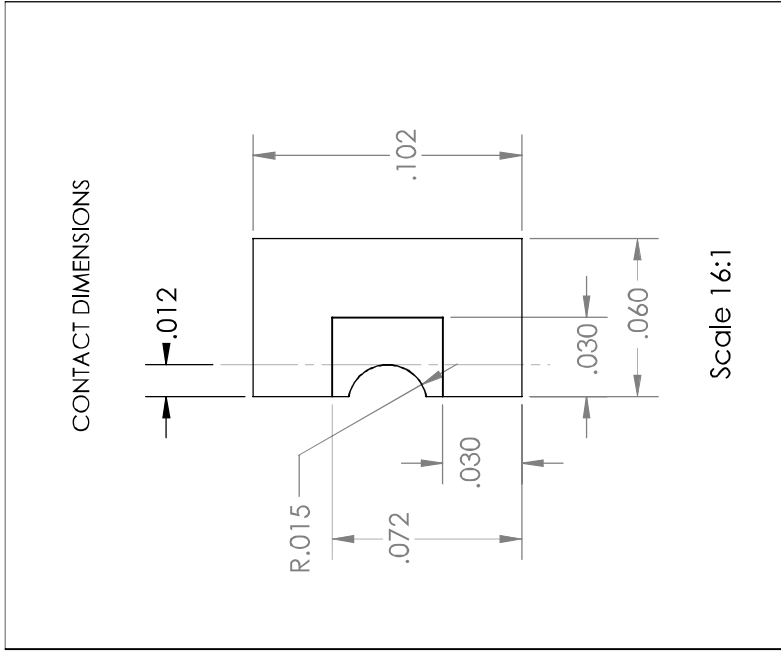
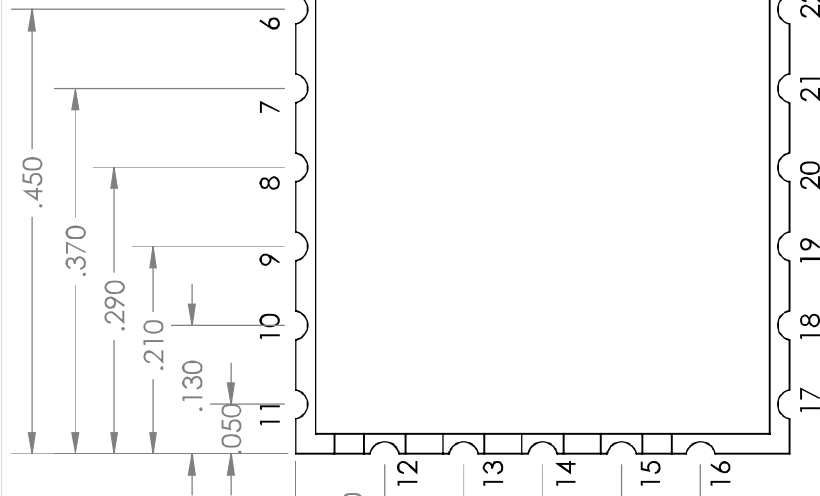
initial rev

<u>PARAMETER</u>	<u>MIN</u>	<u>TYP</u>	<u>MAX</u>	<u>UNITS</u>	<u>NOTES</u>
Frequency Range		1024.000		MHz	1
Step Size		FIXED			7
Lock Time (within 1KHz)			20	msec	2
External Reference (square wave)					
Frequency		160		MHz	9
Amplitude	0.8		V2	V _{p-p}	2
Input Impedance		100		kΩ	2
V1	4.75	5.00	5.25	V	
I1		30	38	mA	1
V2	2.70	3.00	3.30	V	
I2		25	35	mA	1
RF Output Level	-2	0	2	dBm	1
Spurious		-80	-70	dBc	1
Reference Feedthrough		-80	-70	dBc	1
2nd Harmonic		-18	-12	dBc	3
RF Output Impedance		50		Ω	2
Single Sideband Phase Noise					
1 KHz Offset		-109	-104	dBc/Hz	1,8
10 KHz Offset		-110	-105	dBc/Hz	1,8
100 KHz Offset		-130	-125	dBc/Hz	1
1000 KHz Offset		-153	-148	dBc/Hz	1
Operating Temperature	-40		+85	C	3
Lock Indicator ("locked")		TTL		V2	1
Physical Dimensions		SMPLL1			10
Length		0.500		inches	5
Width		0.500		inches	5
Height		0.180		inches	5
Programming		FIXED			7

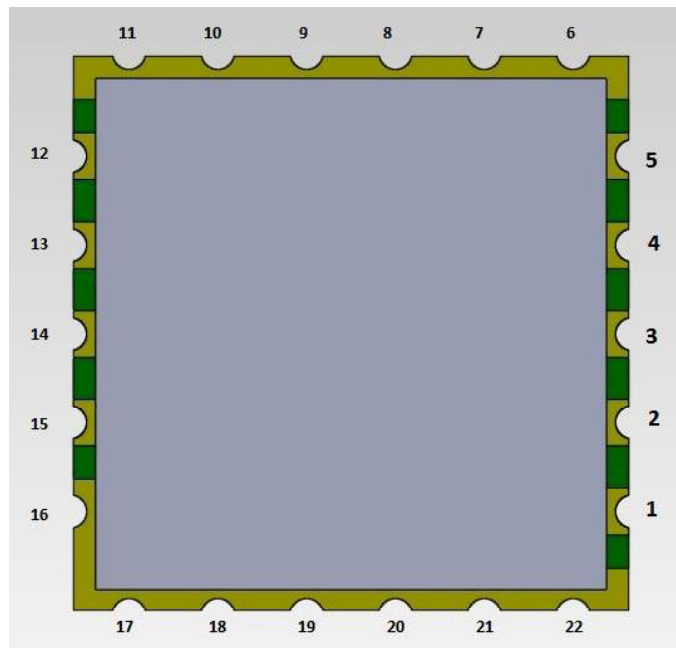
Notes

1. Tested in production, and guaranteed
2. Per current design, not a tested parameter
3. Sample tested from production lot and guaranteed
4. Not a tested or guaranteed design goal
5. By inspection
6. Customer to indicate preference
7. Unit comes on frequency upon turn-on; no programming required and programming ports are open
8. Reference dependant
9. DC Coupled
10. RoHS Compliant

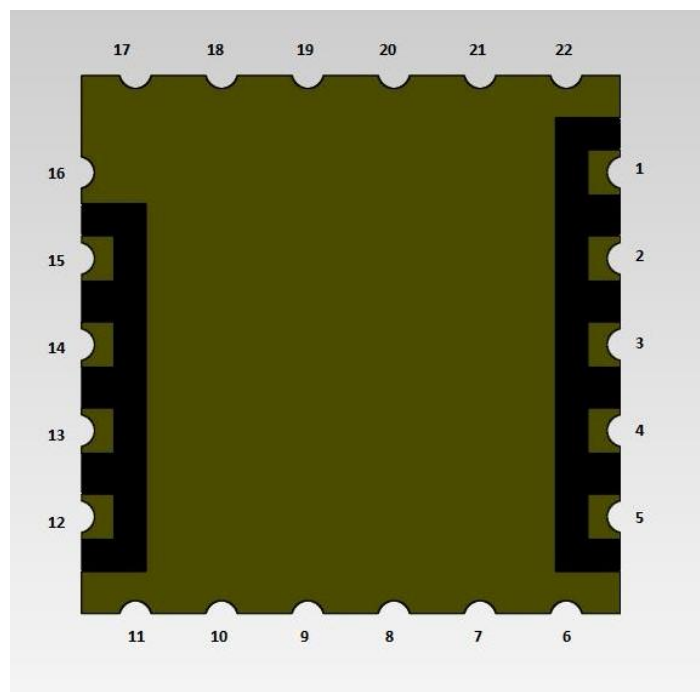




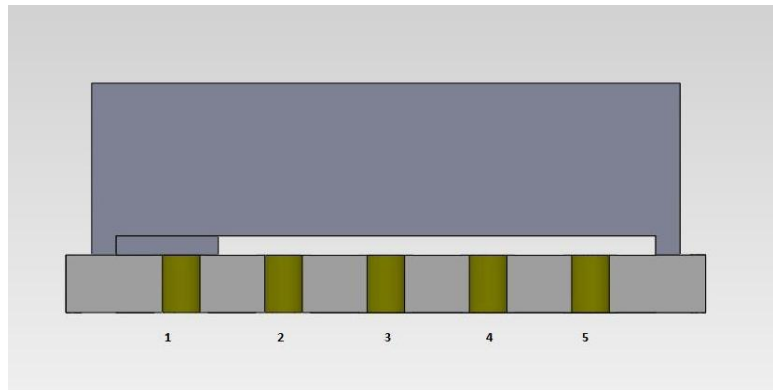
	TITLE: SYN1024SMPL1	
	SIZE	REV
DRAWN: Stefan Babulal DATE: 2/14/2017	DWG. NO. 1	REV
COMMENTS: All dimensions and tolerances are in inches unless otherwise noted		SCALE: 6:1 unit: inch SHEET 2 OF 2



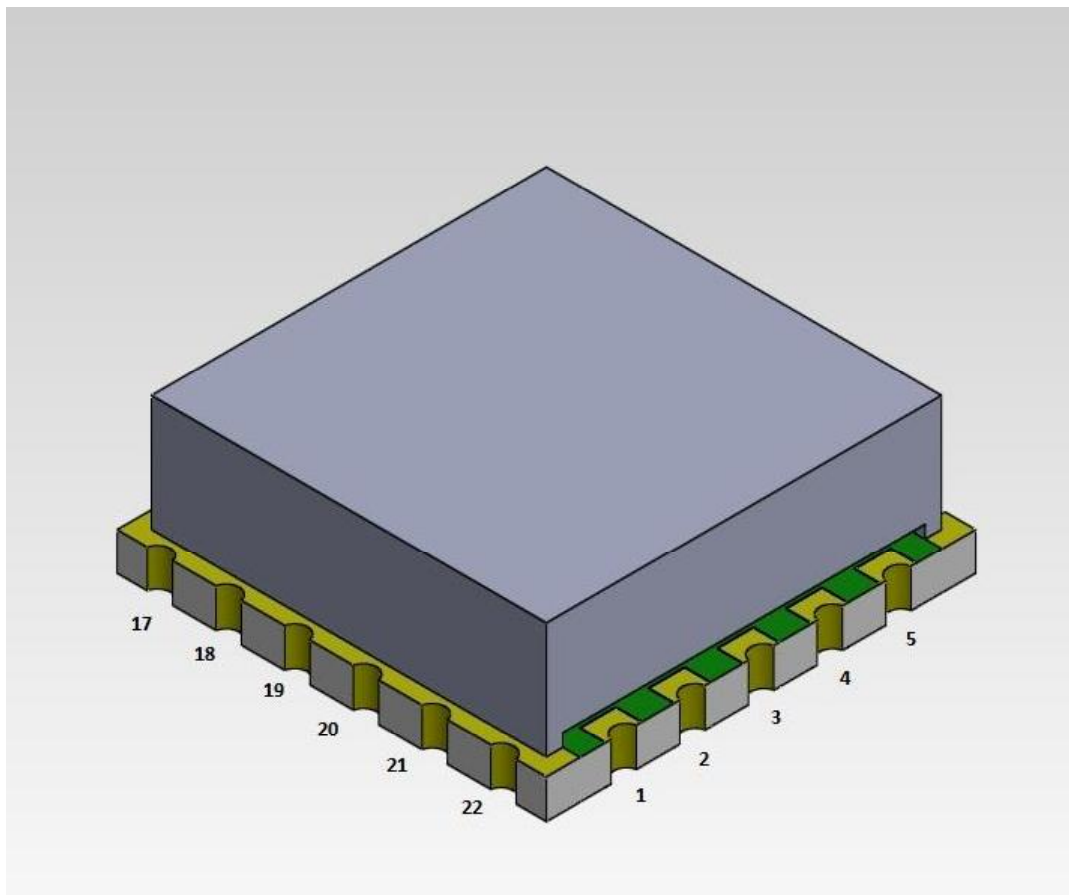
TOP VIEW



BOTTOM VIEW (INVERTED)



SIDE VIEW



ISOMETRIC VIEW